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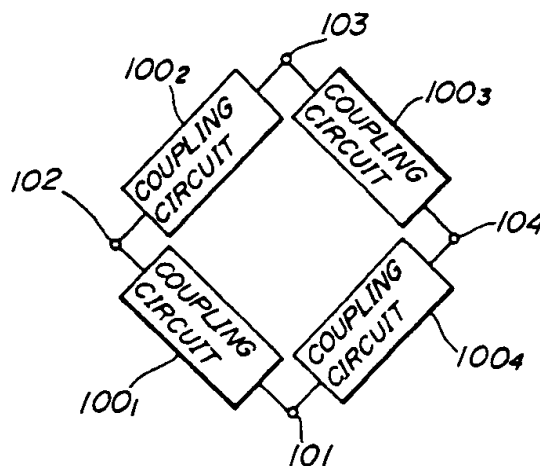
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(54) **Phase inverter and push-pull amplifier using the same.**

(57) A phase inverter includes first, second, third and fourth terminals (101 - 104), a first coupling circuit (100₁) coupled between the first and second terminals, a second coupling circuit (100₂) coupled between the second and third terminals, a third coupling circuit (100₃) coupled between the third and fourth terminals, and a fourth coupling circuit (100₄) coupled between the first and fourth terminals. The first to fourth terminals and the first to fourth coupling circuits are arranged into a ring. The first coupling circuit is of a type different from a type of the fourth coupling circuit. The second and third coupling circuits are identical in type. Two output signals having a phase difference of 180° are drawn from the second and fourth terminal when an input terminal is applied to the first terminal, and an output signal is drawn from the first terminal when two input signals having a phase difference of 180° are applied to the second and fourth terminals. A push-pull amplifier using two phase inverter each having the above-mentioned configuration is also provided.

FIG.3



PHASE INVERTER AND PUSH-PULL AMPLIFIER USING THE SAME

BACKGROUND OF THE INVENTION

The present invention generally relates to a phase inverter, and more particularly to a phase inverter which receives a radio frequency signal and derives two radio frequency signals having a phase difference of 180° therefrom. Further, the present invention is directed to a push-pull amplifier using such a phase inverter.

Conventionally, it is known to form a phase inverter by the use of a transformer, a capacitor or an inductor. Such a phase inverter is suitable for a low-frequency range. In other words, the use of a transformer, a capacitor or an inductor is not suitable for a very high frequency range in the order of tens of giga-hertz or higher because of the presence of stray capacitance. From this reason, it is necessary to use a distributed element circuit such as a coupled line network in the range of very high frequencies (microwave range).

Referring to FIG.1, there is illustrated a conventional phase inverter using parallel coupled lines. Ring-shaped transmission lines 2 and 3 formed of a conductive material are arranged on a dielectric substrate 1 so that two networks each composed of two $\lambda/4$ parallel coupled lines are formed (λ is the wavelength of a signal applied to the transmission line 3). The transmission line 2 has terminals 4₂, 4₃ and 4₄, and the transmission line 3 has a terminal 4₁. Two adjacent terminals among the terminals 4₁, 4₂, 4₃ and 4₄ have a phase difference of 90° . A signal applied to the terminal 4₁ is equally distributed to the terminals 4₂ and 4₄. A signal output from the terminal 4₂ has the $+90^\circ$ phase difference with respect to the input signal, and a signal output from the terminal 4₄ has the -90° phase difference with respect to the input signal. Thus, the signals having a phase difference of 180° are output through the terminals 4₂ and 4₄. In this case, the signals output from the terminals 4₂ and 4₄ are combined together at the terminal 4₃. However, since the signals at the terminals 4₂ and 4₄ are 180° out of phase, no signal is output from the terminal 4₃.

FIG.2A is a graph of amplitude vs. normalized frequency characteristics of the conventional phase inverter shown in FIG.1, and FIG.2B is a graph of phase vs. normalized frequency characteristics thereof. A curve I is a characteristic of a signal passing from the terminal 4₁ to the terminal 4₂. A curve II is a characteristic of a signal passing from the terminal 4₁ to the terminal 4₄. It can be seen from FIG.2A that the amplitude characteristic of the signal passing from the terminal 4₁ to the terminal 4₂ is approximately flat in the $\pm 0.4f_0$ range where

f_0 is the center frequency of this signal (equal to 12.5 GHz, for example). On the other hand, the amplitude characteristic of the signal passing from the terminal 4₁ to the terminal 4₃ is approximately flat only in the $\pm 0.2f_0$ range. Such a range is narrow and insufficient in view of practical board-band use.

It can be seen from FIG.2B that the phase characteristics of the signal passing from the terminal 4₁ to the terminal 4₂ and the signal passing from the terminal 4₁ to the terminal 4₄ have a phase difference of 180° in a range of $\pm 0.4f_0$. From the above-mentioned reasons, it is desired to provide a phase inverter in which the amplitude and phase characteristics of two 180° out-of-phase output signals are both flat in a wider frequency range.

SUMMARY OF THE INVENTION

It is a general object of the present invention to provide an improved phase inverter in which the aforementioned disadvantages are got rid of.

A more specific object of the present invention is to provide a phase inverter in which the amplitude and phase characteristics of two output signals having a phase difference of 180° are both flat in a wider very-high frequency range.

The above objects of the present invention are achieved by a phase inverter comprising first, second, third and fourth terminals, a first coupling circuit coupled between the first and second terminals, a second coupling circuit coupled between the second and third terminals, a third coupling circuit coupled between the third and fourth terminals, and a fourth coupling circuit coupled between the first and fourth terminals. The first to fourth terminals and the first to fourth coupling circuits are arranged into a ring. The first coupling circuit is of a type different from a type of the fourth coupling circuit. The second and third coupling circuits are identical in type. Two output signals having a phase difference of 180° are drawn from the second and fourth terminal when an input terminal is applied to the first terminal, and an output signal is drawn from the first terminal when two input signals having a phase difference of 180° are applied to the second and fourth terminals.

Another object of the present invention is to provide a push-pull amplifier using the above-mentioned phase inverter.

This object of the present invention is achieved by a push-pull amplifier comprising a first phase inverter having an input terminal receiving an input

signal and two output terminals, a second phase inverter having two input terminals and an output terminal outputting an output signal, a first amplifier coupled between one of the two output terminals of the first phase inverter and one of the two input terminals of the second phase inverter, and a second amplifier coupled between the other output terminal of the first phase inverter and the other input terminal of the second phase inverter. Each of the first and second phase inverters includes first, second, third and fourth terminals, a first coupling circuit coupled between the first and second terminals, a second coupling circuit coupled between the second and third terminals, a third coupling circuit coupled between the third and fourth terminals, and a fourth coupling circuit coupled between the first and fourth terminals. The first to fourth terminals and the first to fourth coupling circuits are arranged into a ring. The first coupling circuit is of a type different from a type of the fourth coupling circuit, and the second and third coupling circuits being identical in type. The first, second, and fourth terminals of the first phase inverter are the input terminal and the two output terminals thereof, respectively. The first, second and fourth terminals of the second phase inverter are the two input terminals and the output terminal thereof, respectively. The third terminals of the first and second phase inverters are grounded through first and second resistors, respectively.

Additional objects, features and advantages of the present invention will become apparent from the following detailed description when read in conjunction with the accompanying drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

FIG.1 is a diagram of a conventional phase inverter;

FIG.2A is a graph illustrating amplitude vs. normalized frequency characteristics of the phase inverter shown in FIG.1;

FIG.2B is a graph illustrating phase vs. normalized frequency characteristics of the phase inverter shown in FIG.1;

FIG.3 is a diagram illustrating the principle of the present invention;

FIG.4A is a diagram of a phase inverter according to a first embodiment of the present invention;

FIG.4B is a diagram of another phase inverter according to the first embodiment of the present invention;

FIGS.5A and 5B are diagrams illustrating $\lambda/4$ parallel coupled line networks;

FIG.6A is a diagram of a phase inverter according to a second embodiment of the present

invention;

FIG.6B is a diagram of another phase inverter according to the second embodiment of the present invention;

FIG.7 is a diagram of a directional coupler;

FIG.8A is a graph of amplitude vs. normalized frequency characteristics of the embodiment shown in FIG.6A;

FIG.8B is a graph of phase vs. normalized frequency characteristics of the embodiment shown in FIG.6A;

FIGS.9A through 9D are diagrams of variations of the arrangements shown in FIGS.4A and 4B;

FIG.10A is a diagram of a variation of the arrangements shown in FIGS.4A and 4B;

FIGS.10B and 10C are diagrams of modifications of the first and second embodiments, respectively;

FIG.10D is a diagram of an arrangement equivalent to the arrangement shown in FIG.10B;

FIG.11A is a diagram of a variation of the phase inverter according to the first embodiment of the present invention;

FIGS.11B through 11D are diagrams illustrating structures of transmission lines;

FIG.12 is a diagram illustrating the principle of a phase inverter according to a third embodiment of the present invention in which coupling circuits are formed by waveguide directional couplers;

FIG.13A is a plan view of the third embodiment of the present invention;

FIG.13B is a front view of the third embodiment of the present invention;

FIG.14A is a perspective view of a waveguide directional coupler used in the third embodiment of the present invention;

FIG.14B is a perspective view of another waveguide directional coupler used in the third embodiment of the present invention;

FIGS.15A and 15B are diagrams showing the types of the third embodiment;

FIG.16 is a circuit diagram of a push-pull amplifier to which the phase inverter of the present invention is applied;

FIG.17 is a diagram illustrating a first configuration of the push-pull amplifier according to the present invention;

FIG.18 is a diagram illustrating a second configuration of the push-pull amplifier according to the present invention, and

FIG.19 is a diagram illustrating a third configuration of the push-pull amplifier according to the present invention.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

FIG.3 is a diagram illustrating the principle of the present invention. A phase inverter according to the present invention includes coupling circuits 100₁, 100₂, 100₃ and 100₄ each formed by $\lambda/4$ parallel coupled lines. The coupling circuits 100₁ - 100₄ are arranged into a ring and coupled to each other through terminals 101, 102, 103 and 104 as shown in FIG.3. When two output signals are derived from an input signal, the terminal 101 serves as an input terminal of the phase inverter, and the terminals 102 and 104 serve as output terminals thereof. When an input signal is applied to the input terminal 101, 180° out-of-phase signals are drawn from the output terminals 102 and 104. No signal is drawn from the terminal 103.

The type of the coupling circuit 100₁ between the input terminal 101 and the output terminal 102 is different from that of the coupling circuit 100₄ between the input terminal 101 and the output terminal 104. For example, the coupling circuit 100₁ is formed by open coupled lines, and the coupling circuit 100₄ is formed by grounded coupled lines. On the other hand, the type of the coupling circuit 100₂ is the same as that of the coupling circuit 100₃. That is, the coupling circuits 100₂ and 100₃ are formed by open coupled lines or grounded coupled lines.

Alternatively, the terminals 102 and 104 serve as input terminals, and the terminal 101 serves as an output terminal. In this alternative, two 180° out-of-phase input signals are applied to the terminals 102 and 104 and combined, and an output signal is drawn from the output terminal 101.

FIG.4A is a plan view of a phase inverter according to a first embodiment of the present invention. The phase inverter includes transmission lines 10₁, 10₂, 10₃ and 10₄, each of which is in the shape of a substantially half circle. The transmission lines 10₁ - 10₄ are arranged on a dielectric substrate 1 so as to form a ring. The transmission lines 10₁, 10₂, 10₃ and 10₄ have terminals 20, 21, 22 and 23, respectively. Opposite portions of adjacent transmission lines form $\lambda/4$ parallel coupled lines by which a coupling circuit (network of $\lambda/4$ parallel coupled lines) is formed. That is, four coupling circuits or networks are connected through the terminals 20 - 23 as shown in FIG.4A. In FIG.4A, "o" indicates an open end of a transmission line, and "●" indicates a grounded end of a transmission line.

A network composed of $\lambda/4$ parallel coupled lines is classified into either a double-end open type or a double-end grounded type. FIG.5A shows a $\lambda/4$ parallel coupled line network of the double-end open type, and FIG.5B shows a $\lambda/4$ parallel coupled line network of the double-end grounded type. The $\lambda/4$ parallel coupled line networks shown in FIGS.5A and 5B function as bandpass filters.

The phase difference between a terminal 11 of one of the two transmission lines shown in FIG.5A and a terminal 12 of the other transmission line is equal to + 90° at the center frequency f_0 . There is the - 90° phase difference between a terminal 13 of one of the two transmission lines shown in FIG.5B and a terminal 14 of the other transmission line at the center frequency f_0 .

Turning to FIG.4A, $\lambda/4$ parallel coupled line networks of the double-end open type (coupling circuits) are respectively provided between the terminals 20 and 21, terminals 21 and 22, and terminals 22 and 23. A $\lambda/4$ parallel coupled line network of the double-end grounded type (a coupling circuit) is provided between the terminals 20 and 23. The type of the network between the input terminal 20 and the output terminal 21 is different from the type of the network between the input terminal 20 and the output terminal 23. The network between the terminals 21 and 22 is of the same type as the network between the terminals 22 and 23. When the input signal applied to the input terminal 20, 180° out-of-phase signals having the same amplitude are drawn from the output terminals 21 and 23. Particularly at the center frequency f_0 , there is the + 90° phase difference between the input terminal 20 and the output terminal 21, and the - 90° phase difference between the input terminal 20 and the output terminal 23. In an alternative use of the phase inverter shown in FIG.2A, the terminals 21 and 23 serve as input terminals, and the terminal 20 serves as an input terminal. Two 180° out-of-phase signals applied to the terminals 21 and 23 are combined together so that a combined output signal is obtained at the terminal 20.

The first embodiment of the present invention employs four $\lambda/4$ parallel coupled line networks provided between the individual adjacent terminals. This arrangement of the first embodiment is apparently distinguishable from the conventional arrangement shown in FIG.1. It has been found that the amplitude characteristics of the output signals are flat in an improved range of $\pm 0.3f_0$.

In FIG.2B, a $\lambda/4$ parallel coupled line network of the double-end open type is provided between the input terminal 20 and the output terminal 21, and $\lambda/4$ parallel coupled line networks of the double-end grounded type are provided between the terminals 21 and 22, terminals 22 and 23, and terminals 20 and 23. The type of the network between the input terminal 20 and the output terminal 21 is different from the type of the input terminal 20 and the output terminal 23. The arrangement shown in FIG.4B presents an improved amplitude flat range of $\pm 0.3f_0$.

FIG.6A is a diagram of a phase inverter according to a second embodiment of the present invention. The second embodiment uses directional cou-

plers which are networks each composed of parallel coupled lines. Networks (directional couplers) 35₁, 35₂, 35₃ and 35₄ are of the ring coupler type and are arranged into a ring on the dielectric substrate 1. Terminals 30, 31, 32 and 33 are provided between individual adjacent directional couplers among the directional couplers 35₁ - 35₄. As shown in FIG.7, each of the directional couplers 35₁ - 35₄ has a plurality of comb-shaped interdigital transmission lines. The transmission lines are electrically coupled to each other for every other transmission lines through conducting jump wires 39. The directional coupler has four terminals 35, 36, 37 and 38. In a directional coupler of the open type, the terminal 35 (couple port) and the terminal 36 (main port) are both open. In a directional coupler of the grounded type, the terminals 35 and 36 are both grounded. In the open type directional coupler, there is the 90° phase difference between the terminal 37 (input port) and the terminal 38 (isolation port) at the center frequency f₀. In the grounded type directional coupler, there is the - 90° phase difference between the terminals 37 and 38 at the center frequency f₀. The directional coupler shown in FIG.7 itself is known to a person having an ordinary skill in the art.

Turning to FIG.6A, the directional couplers 35₁, 35₂ and 35₃ respectively provided between the terminals 30 and 31, 31 and 32, and 32 and 33 are of the open type, and the directional coupler 35₄ provided between the terminals 30 and 33 is of the grounded type. When an input signal is applied to the input terminal 30, 180° out-of-phase signals are drawn from the output terminals 31 and 33. Particularly at the center frequency f₀, there is the + 90° phase difference between the terminals 30 and 31, and the -90° phase difference between the terminals 30 and 33.

Three directional couplers shown in FIG.6B provided between the terminals 31 and 32, 32 and 33, and 33 and 30 are of the grounded type, and the remaining directional coupler provided between the terminals 30 and 31 is of the open type. When an input signal is applied to the input terminal 30, 180° out-of-phase signals are drawn from the output terminals 31 and 33. Particularly at the center frequency f₀, there is the + 90° phase difference between the terminals 30 and 31, and the - 90° phase difference between the terminals 30 and 33.

FIG.8A is a graph illustrating amplitude vs. normalized frequency characteristics of signals passing through the phase inverter shown in FIG.6A. FIG.8B is a graph illustrating phase vs. normalized frequency characteristics of signals passing therethrough. A curve III shows frequency characteristics of a signal passing from the input terminal 30 to the output terminal 31, and a curve IV shows frequency characteristics of a signal

passing from the input terminal 30 to the output terminal 33. The amplitude characteristics of the signals III and IV are both flat in the $\pm 0.4f_0$ range (the center frequency f₀ is 12.5GHz, for example). The amplitude characteristics of signals are considerably improved as compared with those for the conventional phase inverter shown in FIG.1. The 180° phase difference between the signals is obtained in the $\pm 0.4f_0$ range. In an alternative use, the terminals 31 and 33 serve as input terminals, and the terminal 30 serves as an output terminal.

FIGS.9A through 9D respectively show variations of the aforementioned first embodiments. A variety of patterns of transmission line is shown.

The transmission lines of the aforementioned individual phase inverters have the same pattern width. For example, the transmission lines 10₁ - 10₄ shown in FIG.4A have the same pattern width. Similarly, as shown in FIG.10A, networks 36 - 39 provided between terminals 40 - 44 are individually composed of two coupled lines having the same pattern width. The terminals 40 - 43 have the same impedance.

A modification of the phase inverter shown in FIG.10A is illustrated in FIG.10B. The phase inverter shown in FIG.10B also functions as an impedance converter. The phase inverter includes networks (coupling circuits) 45, 46, 47 and 48 provided between the terminals 50, 51, 52 and 53. Each of the networks 45 - 48 is formed by a pair of transmission lines W1 and W2. The transmission line W1 is wider than the transmission line W2 (W1 > W2). Each of the terminals 50 and 52 has high impedance (H), and each of the terminals 51 and 53 has low impedance (L). That is, the phase inverter shown in FIG.10B also serves as an impedance converter. When an input signal is applied to the terminal 50, 180° out-of-phase signals are drawn from the terminals 51 and 53.

A phase inverter shown in FIG.10C has networks (directional couplers) 55, 56, 57 and 58 respectively provided between the terminals 60 and 61, 61 and 62, 62 and 63, and 60 and 63. Each of the directional couplers 55 - 58 is formed by three transmission lines W1, W2 and W3. The transmission lines W1 and W3 having the same pattern width are mutually connected through jump wires. The transmission lines W1 and W3 are wider than the transmission line W2. Each of the terminals 60 and 62 has high impedance, and each of the terminals 61 and 63 has low impedance. That is, the phase inverter shown in FIG.10C also functions as an impedance converter.

The phase inverter shown in FIG.10B is equivalent to a phase inverter shown in FIG.10D. The phase inverter depicted in FIG.10D has the phase inverter shown in FIG.10A. A transformer 65 extends from the phase inverter to the terminal 41.

The transformer 65 has two stepwise line portions 65a and 65b each having a length of $\lambda/4$. The line portion 65b connected to the terminal 41 is wider than the line portion 65a. Impedance of the terminal 41 is less than that of the terminal 40. Similarly, a transformer 66 having two line portions 66a and 66b extends from the phase inverter to the terminal 43. The terminal 43 has low impedance as compared with that of the terminal 40. The signals drawn from the terminals 41 and 43 are 180° out of phase.

The transmission lines shown in FIGS. 4A, 4B, 9A - 9D, and 10A - 10D are not limited to curved transmission lines. As shown in FIG. 11A, straight transmission lines can be used instead of curved transmission lines.

The aforementioned transmission lines are conductive patterns formed on a dielectric substrate having a dielectric constant ϵ , as shown in FIG. 11B, which illustrates a microstrip structure. Alternatively, the transmission lines are formed by a coplanar waveguide as shown in FIGS. 11C and 11D. Ground planes are grounded. A signal passes through the dielectric substrate. Thus, a loss of signal is not negligible in the order of tens of giga-hertz. A waveguide directional coupler is suitable for a very-high frequency range of tens of giga-hertz or higher.

FIG. 12 is a diagram of a configuration of the phase inverter in which the coupling circuits shown in FIG. 3 are formed by waveguide directional circuits. Referring to FIG. 12, waveguide directional couplers 70_1 , 70_2 , 70_3 and 70_4 are coupled through three-branch waveguides 71_1 , 71_2 , 71_3 and 71_4 . Terminals 72_1 , 72_2 , 72_3 and 72_4 are connected to the three-branch waveguides 71_1 , 71_2 , 71_3 and 71_4 , respectively. In actuality, one of the three branches of each of the three-branch waveguides 71_1 - 71_4 serves as the corresponding input or output terminal. Short-circuit elements 73_1 , 73_2 , 73_3 and 73_4 are coupled to the waveguide directional couplers 70_1 , 70_2 , 70_3 and 70_4 , respectively. Each of the waveguide directional couplers 70_1 - 70_4 has an input port, a main port, a couple port and an isolation port. The main port and the couple port of each of the waveguide directional couplers 70_1 - 70_4 are short-circuited by the respective short-circuit elements 73_1 - 73_4 . The input and isolation ports are coupled to two of the three branches of the corresponding three-branch waveguides. The waveguide directional couplers 70_1 - 70_4 have the same length. The reflection angle of each of the short-circuit elements 73_1 , 73_2 and 73_3 is different by 180° from the reflection angle of the short-circuit element 73_4 . An input signal is applied to the terminal 72_1 . Two output signals each having the same amplitude of the input signal are drawn from the terminals 72_2 and 72_4 . There is

the 180° difference between the signals output from the terminals 72_2 and 72_4 .

FIGS. 13A and 13B are respectively plan and front views of a phase inverter according to a third embodiment of the present invention. The illustrated phase inverter uses waveguide directional couplers arranged as shown in FIG. 12. Waveguide directional couplers 80_1 , 80_2 , 80_3 and 80_4 are of the magnetic field coupling type and correspond to the waveguide directional couplers 70_1 , 70_2 , 70_3 and 70_4 , respectively. A broken line represents an inner wall. Three-branch waveguides 81_1 , 81_2 , 81_3 and 81_4 are arranged into a ring and couples the waveguide directional couplers 80_1 - 80_4 , which extend radially. An elongated waveguide 82 having a length of $\lambda_0/4$ ($\lambda_0/4$ is the wavelength within the waveguide 82) is directly connected to an end of the waveguide directional coupler 80_4 . Short-circuit plates 83_1 , 83_2 , 83_3 are provided for ends of the waveguide directional couplers 80_1 , 80_2 and 80_3 , respectively, so that the main and couple ports thereof are short-circuited. A short-circuit plate 83_4 is provided for an end of the elongated waveguide 82 so that main and couple ports thereof are short-circuited. No short-circuit plate is fastened to the end of the waveguide directional coupler 80_4 . Terminals 84_1 , 84_2 , 84_3 and 84_4 are connected to the three-branch waveguides 81_1 , 81_2 , 81_3 and 81_4 , respectively.

Each of the waveguide directional couplers 80_1 - 80_4 has a structure as shown in FIG. 14A. The one-dotted chain lines shown in FIG. 14A represent the elongated waveguide 82 shown in FIG. 13A. Each of the waveguide directional couplers 80_1 - 80_4 has an input port A, a main port B, a couple port C and an isolation port D. As illustrated by the arrows, a signal input through the input port A is output through the main port B and the couple port C. Signals which are respectively input through the main port and the coupled port C are combined and then drawn through the isolation port D. A letter "S" indicates the short-circuit plate 83_4 (a short-circuit plate provided for the end of the waveguide directional coupler is omitted for the sake of simplicity), a reference G_M indicate a main guide, and a reference G_A indicates an auxiliary guide, and a reference "P" indicates a plane of symmetry of magnetic field.

Turning to FIG. 13A, a signal supplied to the terminal 84_1 (Δ) is equally distributed so that 180° out-of-phase signals are output from the terminals 84_2 (P1) and 84_4 (P2). No signal is output from the terminal 84_3 (Σ). Alternatively, 180° out-of-phase signals supplied to the terminals 84_2 (P1) and 84_4 (P2) are combined so that a signal is output from the terminal 84_1 (Δ). The short-circuit plate 83_1 is directly connected to the waveguide directional coupler 80_1 . Alternatively, a short-circuit element of

a waveguide having a length l equal to $(\lambda_0/4)n$ and a short-circuit plate connected to an end thereof may be used where 'n' is an integer. When $n = 0$, the short-circuit plate 83₁ is directly connected to the end of the waveguide directional coupler 80₁. When the short-circuit element of a length l equal to $(\lambda_0/4)n$ is used, the elongated waveguide 82 having a length of $\lambda_0/4$ is replaced by an elongated waveguide having a length l of $(\lambda_0/4)(n + 1)$ and having a short-circuit plate connected to an end thereof. In this alternative, there is the 180° phase difference between the path between the input port A and the isolation port D of the waveguide directional coupler 80₁ and the path between the input port A and the isolation port D of the waveguide directional coupler 80₂. A signal applied to the elongated waveguide 82 is reflected by the short-circuit plate 83₂ and is then output therefrom so that a phase difference of $(\lambda_0/4) \times 2 = 180^\circ$ can be generated.

Alternatively, each of the waveguide directional couplers 70₁ - 70₄ (FIG.12) may be formed by an electric field coupling type waveguide directional coupler as shown in FIG.14B, in which those parts which have the same functions as those in FIG.14A are given the same references. The waveguide directional coupler shown in FIG.14A has two waveguides arranged side by side. On the other hand, the waveguide directional coupler shown in FIG.14B has waveguides elevationally stacked.

FIG.15A is diagram showing the type of each of the waveguide directional couplers shown in FIGS.13A and 13B. Each of the waveguide directional couplers 80₁, 80₂ and 80₃ is of the double-end grounded type, and the combination of the waveguide directional coupler 80₄ and the elongated waveguide 82 having the short-circuit plate 83₂ (elongated waveguide short-circuit element) is of the double-end open type. Alternatively, as shown in FIG.15B, it is possible to employ three waveguide directional couplers of the double-end open type and a waveguide directional coupler of the double-end grounded type. In the alternative, a $\lambda_0/4$ elongated waveguide having a short-circuit plate is directly connected to an end of each of the waveguide directional coupler of the double-end open type, and a short-circuit plate is directly connected to an end of the remaining waveguide directional coupler of the double-end grounded type. It can be seen from the above description that the third embodiment of the present invention has four variations. The first and second variations are phase inverters having the arrangement shown in FIG.15A in which the waveguide directional couplers are of the magnetic field coupling type and the electric field coupling type. The third and fourth variations are phase inverters having the arrangement shown in FIG.15B in which the waveguide directional cou-

plers are of the magnetic field coupling type and the electric field coupling type.

FIG.16 is a circuit diagram of a push-pull amplifier which uses two phase inverters according to the present invention. The illustrated push-pull amplifier has two phase inverters 90 and 91 configured according to the present invention. The phase inverter 90 has a single input terminal and two output terminals, and the phase inverter 91 has two input terminals and a single output terminal. An amplifier A1 is provided between one of the two output terminals of the phase inverter 90 and one of the two input terminals of the phase inverter 91. An amplifier A2 is provided between the other output terminal of the phase inverter 90 and the other input terminal of the phase inverter 91. An RF (radio frequency) signal a is applied to the phase inverter 90, which outputs two output signals b and c having a phase difference of 180° . The signals b and c are amplified by the amplifiers A1 and A2, respectively, and supplied to the phase inverter 91. The amplified signals b and c are combined by the phase inverter 91, which generates an RF output signal d . The amplifiers A1 and A2 are B-class amplifiers. Thus, each of the amplifiers A1 and A2 amplifies either one of the polarities of the amplified signals b and c . The phase of amplified signal b is inverted by 180° by the phase inverter 91 so that the RF output signal d has both the polarities. The phase inverters 90 and 91 have middle-point ground terminals 92 and 93, respectively so that the reference D.C. level of the output signal b is equal to that of the output signal c .

FIG.17 is a diagram of a push-pull amplifier in which the phase inverters 90 and 91 are formed by $\lambda/4$ parallel coupled line networks according to the first embodiment of the present invention. Each of the phase inverters has one grounded-coupled lines network between the terminals 21 and 23. Thus, it is considered that a middle-point ground terminal is substantially provided between the networks provided between the terminals 21 and 23. The terminal 22 of the phase inverter 90 which is grounded through a resistor R1 (having a resistance of 50 ohms, for example) corresponds to the middle-point ground terminal 92 shown in FIG.16. The terminal 22 of the phase inverter 91 which is grounded through a resistor R2 (having a resistance of 50 ohms, for example) corresponds to the middle-point ground terminal 93 shown in FIG.16. An RF signal is applied to the terminal 20 of the phase inverter 90. The B-class amplifier A1 is connected between the terminal 21 of the phase inverter 90 and the terminal 23 of the phase inverter 91. The B-class amplifier A2 is connected between the terminal 23 of the phase inverter 90 and the terminal 21 of the phase inverter 91. The RF output signal is drawn from the terminal 20 of

the phase inverter 91.

FIG.18 is a diagram of a push-pull amplifier in which the phase inverters 90 and 91 are formed by directional couplers according to the second embodiment of the present invention. An RF input signal is applied to the terminal 30 of the phase inverter 90, which outputs 180° out-of-phase signals through the terminals 31 and 33. The signals drawn from the terminals 31 and 33 pass through the B-class amplifier 31 and 33, and then applied to the terminals 33 and 31 of the phase inverter 91, respectively. The RF signal is drawn from the terminal of the phase inverter 91. The terminals 32 of the phase inverters 90 and 91 are grounded through the resistors R1 and R2, respectively. The terminals 32 of the phase inverters 90 and 91 correspond to the middle-point ground terminals 92, and 93, respectively.

FIG.19 is a diagram of a push-pull amplifier in which the phase inverters 90 and 91 are formed by waveguide directional couplers according to the third embodiment of the present invention. An RF input signal is applied to one of the three branches of the three-branch waveguide 84₁ of the phase inverter 90. One of the three branches of the three-branch waveguide 84₂ is coupled to the B-class amplifier A1 through a waveguide 86₁. One of the three branches of the three-branch waveguides 84₄ of the phase inverter 90 is coupled to the B-class amplifier A2 through a waveguide 86₃. A non-reflection terminator 85 having a resistance of 50 ohms is connected to one of the three branches of the three-branch waveguide 84₃ of the phase inverter 90. The B-class amplifier A1 is connected to one of the three branches of the three-branch waveguide 84₄ of the phase inverter 91 through a waveguide 86₂, and the B-class amplifier A2 is connected to one of the three-branches of the three-branch waveguide 84₂ of the phase inverter 91 through a waveguide 86₄. A non-reflection terminator 87 having a resistance of 50 ohms is connected to the one of the three branches of the three-branch waveguide 84₃ of the phase inverter 91. The short-circuit elements 73₁, 73₂, 73₃ and 73₄ (FIG.12) serving as the middle-point ground terminals 92 and 93 (FIG.16) are provided so that the reference D.C. level of the output signal b is equal to that of the output signal c.

The present invention is applied to not only the push-pull amplifier but also a mixer, a mono-pulse comparator and the like.

The present invention is not limited to the aforementioned embodiments, and variations and modifications may be made without departing from the scope of the present invention.

Claims

A phase inverter comprising:

first, second, third and fourth terminals (101 - 104);
a first coupling circuit (100₁) coupled between said first and second terminals;

a second coupling circuit (100₂) coupled between said second and third terminals;

a third coupling circuit (100₃) coupled between said third and fourth terminals; and

a fourth coupling circuit (100₄) coupled between said first and fourth terminals,

said first to fourth terminals and said first to fourth coupling circuits being arranged into a ring,

said first coupling circuit being of a type different from a type of said fourth coupling circuit, and said second and third coupling circuits being identical in type.

wherein two output signals having a phase difference of 180° are drawn from said second and fourth terminal when an input terminal is applied to said first terminal, and an output signal is drawn from said first terminal when two input signals having a phase difference of 180° are applied to said second and fourth terminals.

2. A phase inverter as claimed in claim 1, characterized in that said first coupling circuit (100₁) has two ends which are both open, and said fourth coupling circuit has two ends which are grounded so that said first coupling circuit is different in type from said fourth coupled circuit.

3. A phase inverter as claimed in claim 1, characterized in that said first coupling circuit (100₁) has two ends which are both grounded, and said fourth coupling circuit (100₄) has two ends which are both open so that said first coupling circuit is different in type from said fourth coupling circuit.

4. A phase inverter as claimed in any of claims 1 to 3, characterized in that each of said second and third coupling circuits (100₂, 100₃) has two ends which are both open so that said second and third coupling circuits are of the same type.

5. A phase inverter as claimed in any of claims 1 to 3, characterized in that each of said second and third coupling circuits (100₂, 100₃) has two ends which are both grounded so that said second and third coupling circuits are of the same type.

6. A phase inverter as claimed in any of claims 1 to 5, characterized in that each of said first to fourth coupling circuits (100₁ - 100₄) comprises two coupled lines (10₁ - 10₄) each having a length of $\lambda/4$ where λ is a wavelength of each of said input and output signals.

7. A phase inverter as claimed in any of claims 1 to 5, characterized in that each of said first to fourth coupling circuits (100₁ - 100₄) comprises three or more $\lambda/4$ interdigital coupled lines (35₁ - 35₄) where λ is a wavelength of each of said input

and output signals.

8. A phase inverter as claimed in any of claims 1 to 5, characterized in that each of said first to fourth coupling circuits (100₁ - 100₄) comprises a directional coupler (35₁ - 35₄).

9. A phase inverter as claimed in claim 6, characterized in that said two coupled lines (10₁ - 10₄) have an identical width.

10. A phase inverter as claimed in claim 6, characterized in that one (W1) of said two coupled lines is wider than the other coupled line (W2).

11. A phase inverter as claimed in any of claims 1 to 5, characterized in that:

each of said first to fourth coupling circuits (100₁ - 100₄) has a first line (W2) and a second line (W1) wider than said first line,

said first and second lines each have a length of $\lambda/4$ where λ is a wavelength of each of said input and output signals and are arranged in parallel to thereby form coupled lines,

said first line of each of said first and fourth coupling circuit is coupled to said first terminal (50),

said first line of each of said second and third coupling circuit is connected to said third terminal (52),

said second line of each of said first and second coupling circuits is coupled to said second terminal (51),

said second line of each of said third and fourth coupling circuits is coupled to said fourth terminal (53), and

an impedance of each of said second and fourth terminals is lower than that of each of said first and third terminals.

12. A phase inverter as claimed in any of claims 1 to 5, characterized in that:

each of said first to fourth coupling circuits (100₁ - 100₄) has a first group of lines mutually connected,

a second group of lines (W1, W3) mutually connected,

the lines of said first and second groups have an interdigital arrangement,

the lines of said second group are wider than the lines of said first group,

said first group of lines of each of said first and fourth coupling circuit is coupled to said first terminal (60),

said first group of lines of each of said second and third coupling circuit is connected to said third terminal (62),

said second group of lines of each of said first and second coupling circuits is coupled to said second terminal (61),

said second group of lines of each of said third and fourth coupling circuits is coupled to said fourth terminal (63), and

an impedance of each of said second and fourth terminals is lower than that of each of said first and third terminals.

13. A phase inverter as claimed in any of claims 1 to 5, characterized in that said first to fourth coupling circuits (100₁ - 100₄) respectively comprises first to fourth waveguide directional couplers (70₁ - 70₄) each having a short-circuit element (73₁ - 73₄) and having input (A), main (B), couple (C) and isolation (D) ports, and first to fourth branch waveguides (71₁ - 71₄) each having first, second and third branches, and in that:

said first branch waveguide (71₁) couples said first and fourth waveguide directional couplers (70₁, 70₄) through said input and isolation ports thereof and said first and second branches of said first branch waveguide, the third branch thereof serving as said first terminal (72₁),

said second branch waveguide (71₂) couples said first and second waveguide directional couplers (70₁, 70₂) through said input and isolation ports and said first and second branches of said second branch waveguide, the third branch thereof serving as said second terminal (72₂),

said third branch waveguide (71₃) couples said second and third waveguide directional couplers (70₂, 70₃) through said input and isolation ports thereof and said first and second branches of said third branch waveguide, the third branch thereof serving as said third terminal (72₃),

said fourth branch waveguide (71₄) couples said third and fourth waveguide directional couplers (70₃, 70₄) through said input and isolation ports and said first and second branches of said fourth branch waveguide, the third branch thereof serving as said fourth terminal (71₄), and

said short-circuit element (73₁ - 73₄) is provided for said main and couple ports of each of said first to fourth waveguide directional couplers.

14. A phase inverter as claimed in claim 13, characterized in that said short-circuit element for said fourth waveguide directional coupler (80₄) has an elongated waveguide (82) directly coupled to said main and couple ports of said fourth waveguide directional coupler, and a short-circuit plate (83₄) which short-circuits said elongated waveguide, and in that said short-circuit element for each of said first to third waveguide directional couplers (80₁ - 80₃) includes a short-circuit element (84₁ - 84₃) which short-circuits said main and couple ports of each of said first to third waveguide directional couplers.

15. A phase inverter as claimed in claim 13, characterized in that said short-circuit element (71₁ - 73₃) provided for each of said first to third waveguide directional couplers (70₁ - 70₃) includes an elongated waveguide (82) directly coupled to said main and couple ports thereof and a short-

circuit plate (83₁ - 83₃) short-circuiting said elongated waveguide, and in that said short-circuit element provided for said fourth waveguide directional coupler (70₄) includes a short-circuit plate (83₄) which short-circuits said main and couple ports thereof.

16. A phase inverter as claimed in claim 14, characterized in that said elongated waveguide (82) has a length corresponding to the 180° phase difference between said first and fourth terminals.

17. A phase inverter as claimed in claim 15, characterized in that each of said elongated waveguides (82) has a length corresponding to the 180° phase difference between said first and fourth terminals.

18. A phase inverter as claimed in any of claims 13 to 17, characterized in that each of said first to fourth waveguide directional couplers is selected from the group consisting of a magnetic field coupling type waveguide directional coupler (FIG.14A) and an electric field coupling type waveguide directional coupler (FIG.14B).

19. A push-pull amplifier comprising:
a first phase inverter (90) having an input terminal receiving an input signal and two output terminals;
a second phase inverter (91) having two input terminals and an output terminal outputting an output signal;
a first amplifier (A1) coupled between one of said two output terminals of said first phase inverter and one of said two input terminals of said second phase inverter; and
a second amplifier (A2) coupled between the other output terminal of said first phase inverter and the other input terminal of said second phase inverter, each of said first and second phase inverters including:
first, second, third and fourth terminals (101- 104);
a first coupling circuit (100₁) coupled between said first and second terminals;
a second coupling circuit (100₂) coupled between said second and third terminals;
a third coupling circuit (100₃) coupled between said third and fourth terminals; and
a fourth coupling circuit (100₄) coupled between said first and fourth terminals,
said first to fourth terminals and said first to fourth coupling circuits being arranged into a ring,
said first coupling circuit being of a type different from a type of said fourth coupling circuit, and said second and third coupling circuits being identical in type,
said first, second, and fourth terminals of said first phase inverter being said input terminal and said two output terminals thereof, respectively,
said first, second and fourth terminals of said second phase inverter being said two input terminals and said output terminal thereof, respectively, and

said third terminals of said first and second phase inverters being grounded through first and second resistors (R1, R2), respectively.

20. A push-pull amplifier as claimed in claim 19, characterized in that said first coupling circuit (100₁) has two ends which are both open, and said fourth coupling circuit (100₄) has two ends which are grounded so that said first coupling circuit is different in type from said fourth coupling circuit.

21. A push-pull amplifier as claimed in claim 19, characterized in that said first coupling circuit (100₁) has two ends which are both grounded, and said fourth coupling circuit (100₄) has two ends which are both open so that said first coupling circuit is different in type from said fourth coupling circuit.

22. A push-pull amplifier as claimed in claim 19, characterized in that each of said second and third coupling circuits (100₂, 100₃) has two ends which are both open so that said second and third coupling circuits are of the same type.

23. A push-pull amplifier as claimed in claim 19, characterized in that each of said second and third coupling circuits (100₂, 100₃) has two ends which are both grounded so that said second and third coupling circuits are of the same type.

24. A push-pull amplifier as claimed in any of claims 19 to 23, characterized in that each of said first to fourth coupling circuits (100₁ - 100₄) comprises two coupled lines (10₁ - 10₄) each having a length of $\lambda/4$ where λ is a wavelength of each of said input and output signals.

25. A push-pull amplifier as claimed in any of claims 19 to 23, characterized in that each of said first to fourth coupling circuits (100₁ - 100₄) comprises three or more $\lambda/4$ interdigital coupled lines (35₁ - 35₄) where λ is a wavelength of each of said input and output signals.

26. A push-pull amplifier as claimed in any of claims 19 to 23, characterized in that each of said first to fourth coupling circuits (100₁ - 100₄) comprises a directional coupler (35₁ - 35₄).

27. A push-pull amplifier as claimed in any of claims 19 to 23, characterized in that each of said first to fourth coupling circuits (100₁ - 100₄) comprises a waveguide directional amplifier (80₁ - 80₄).

28. A push-pull amplifier as claimed in any of claims 19 to 27, characterized in that each of said first and second amplifiers (A1, A2) has a B-class amplifying function.

FIG. 1
PRIOR ART

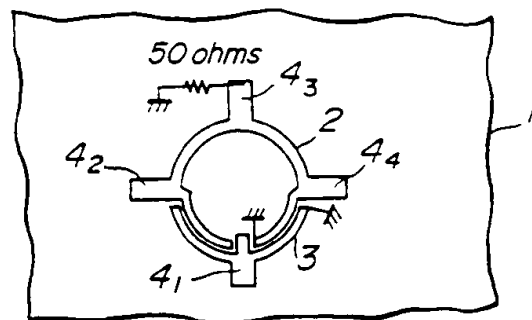


FIG. 2A
PRIOR ART

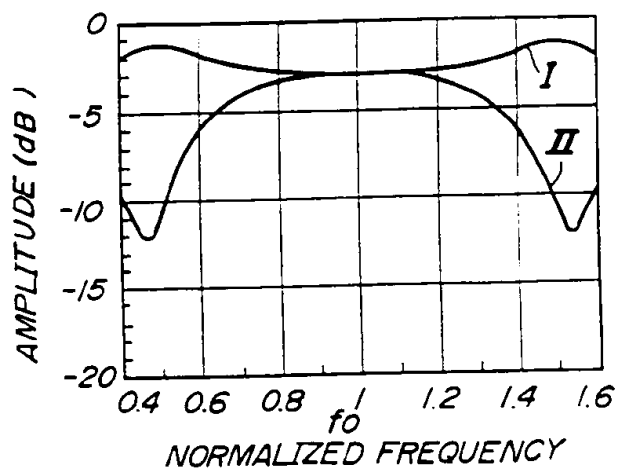


FIG. 2B
PRIOR ART

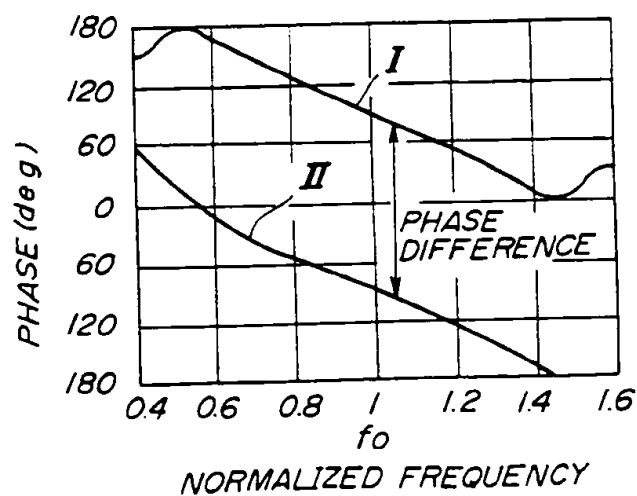


FIG.3

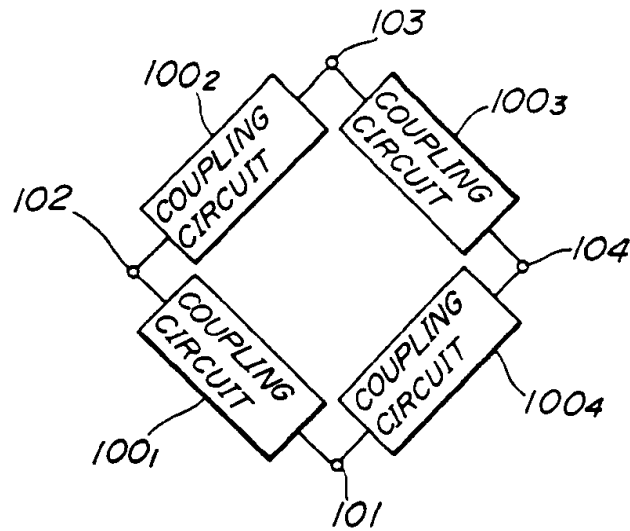


FIG.4A

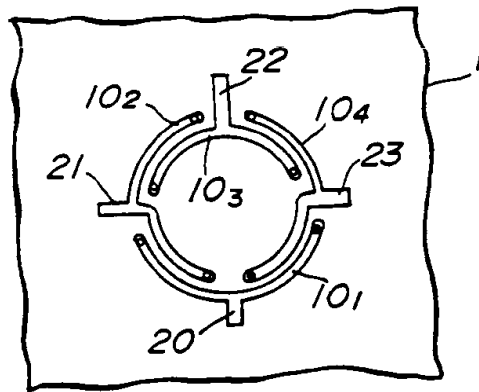
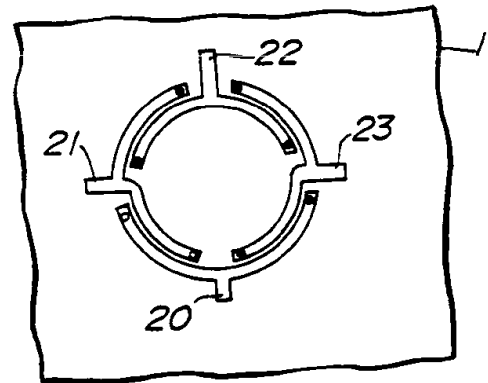


FIG.4B



• GROUNDED
• OPEN

FIG. 5A

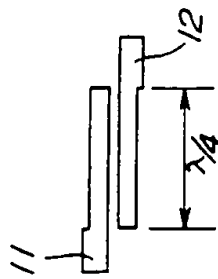


FIG. 5B

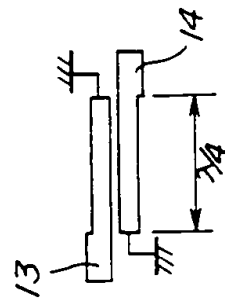


FIG. 6A

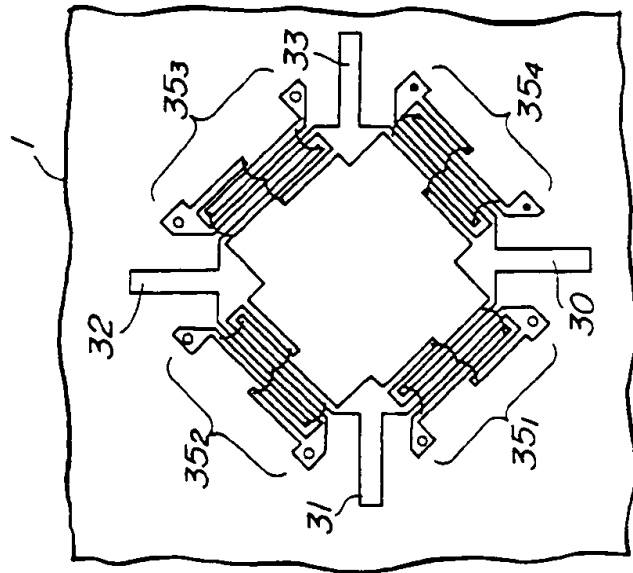


FIG. 6B

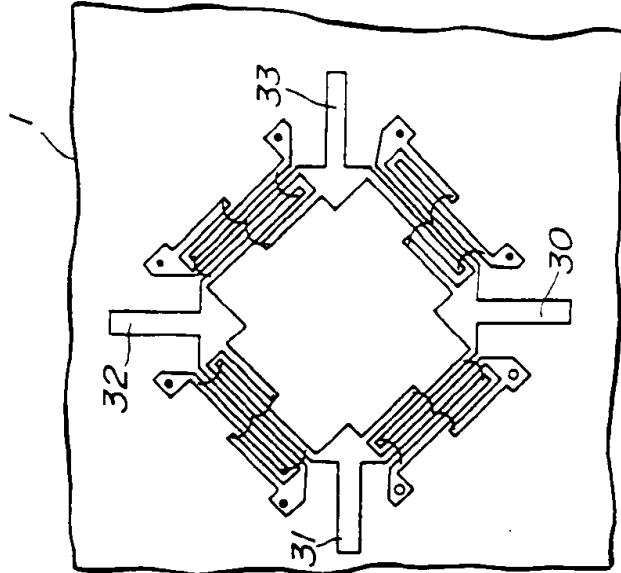


FIG. 7

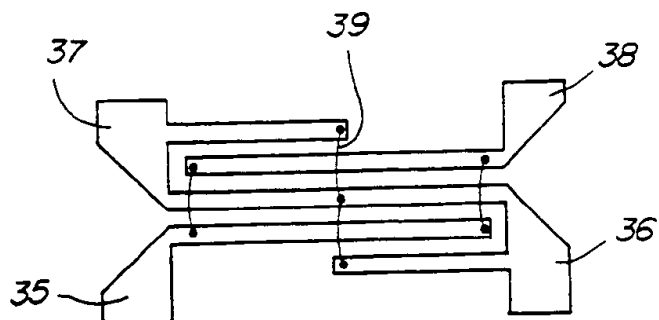


FIG. 8A

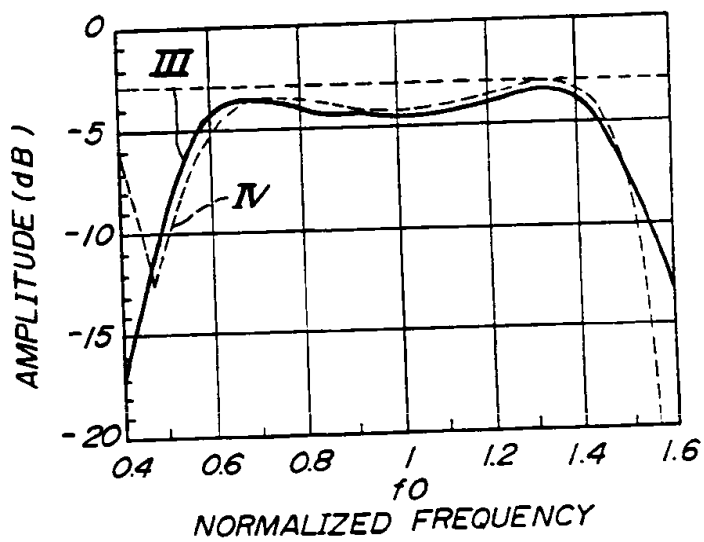


FIG. 8B

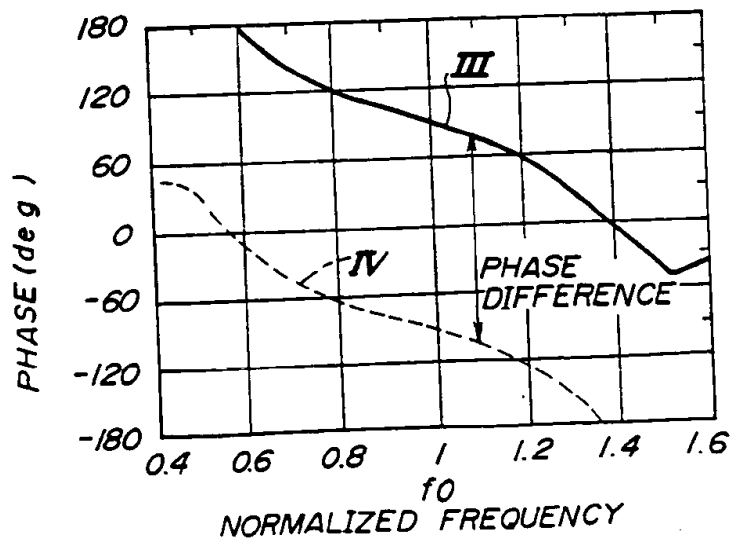


FIG. 9A

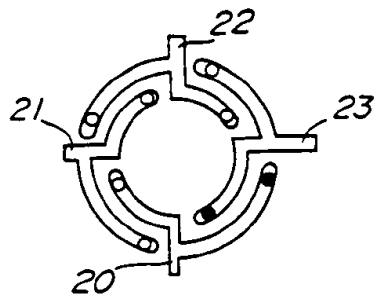


FIG. 9B

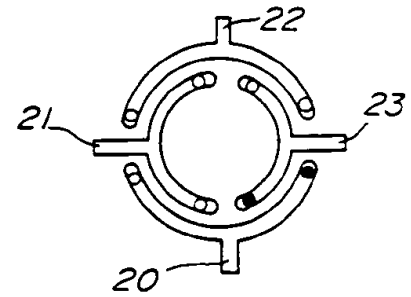


FIG. 9C

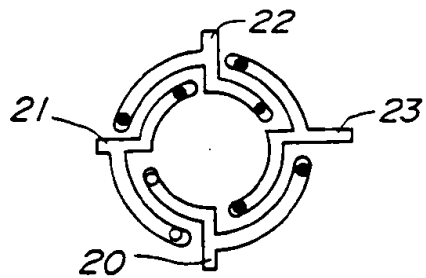


FIG. 9D

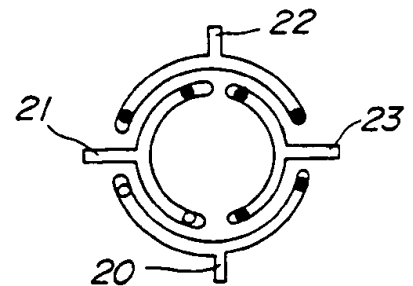


FIG. 10A

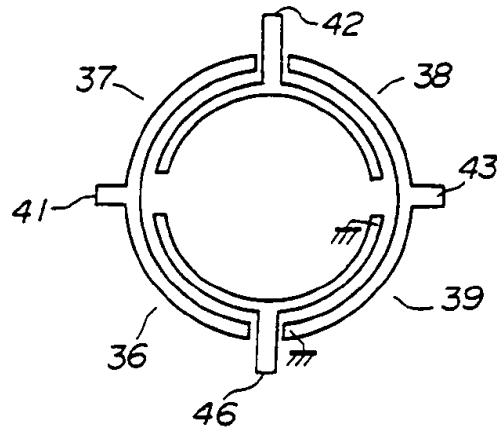


FIG. 10B

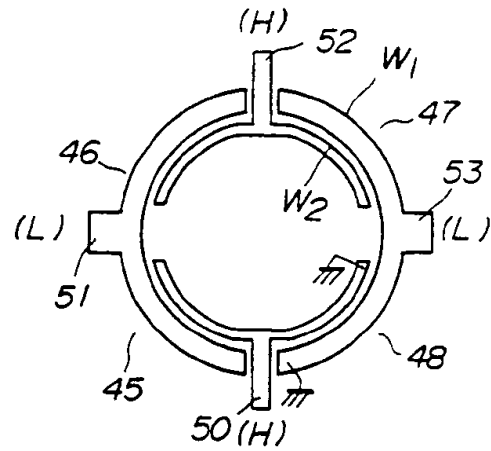


FIG. 10C

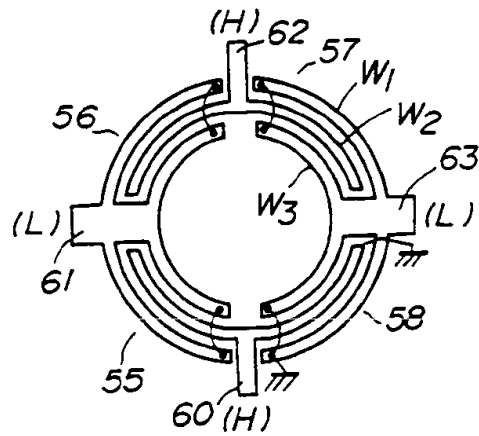


FIG. 10D

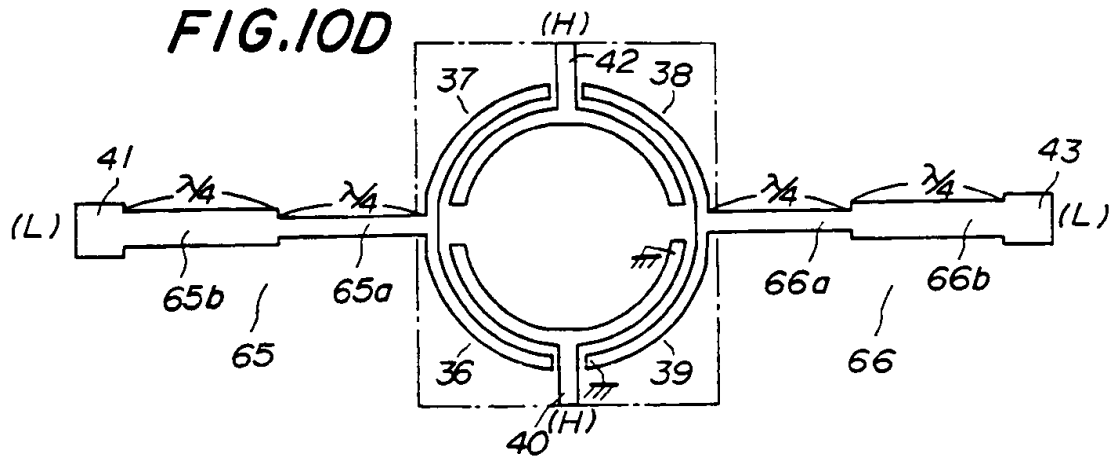


FIG. 11A

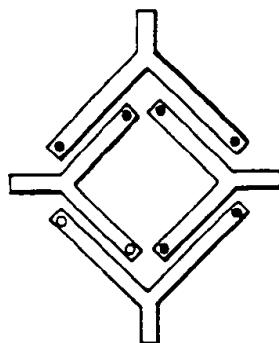


FIG. 11B

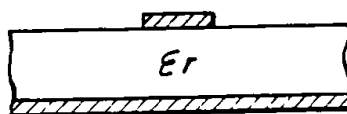


FIG. 11C

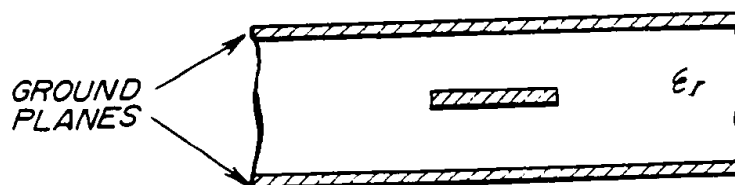


FIG. 11D

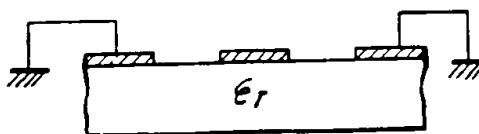


FIG. 12

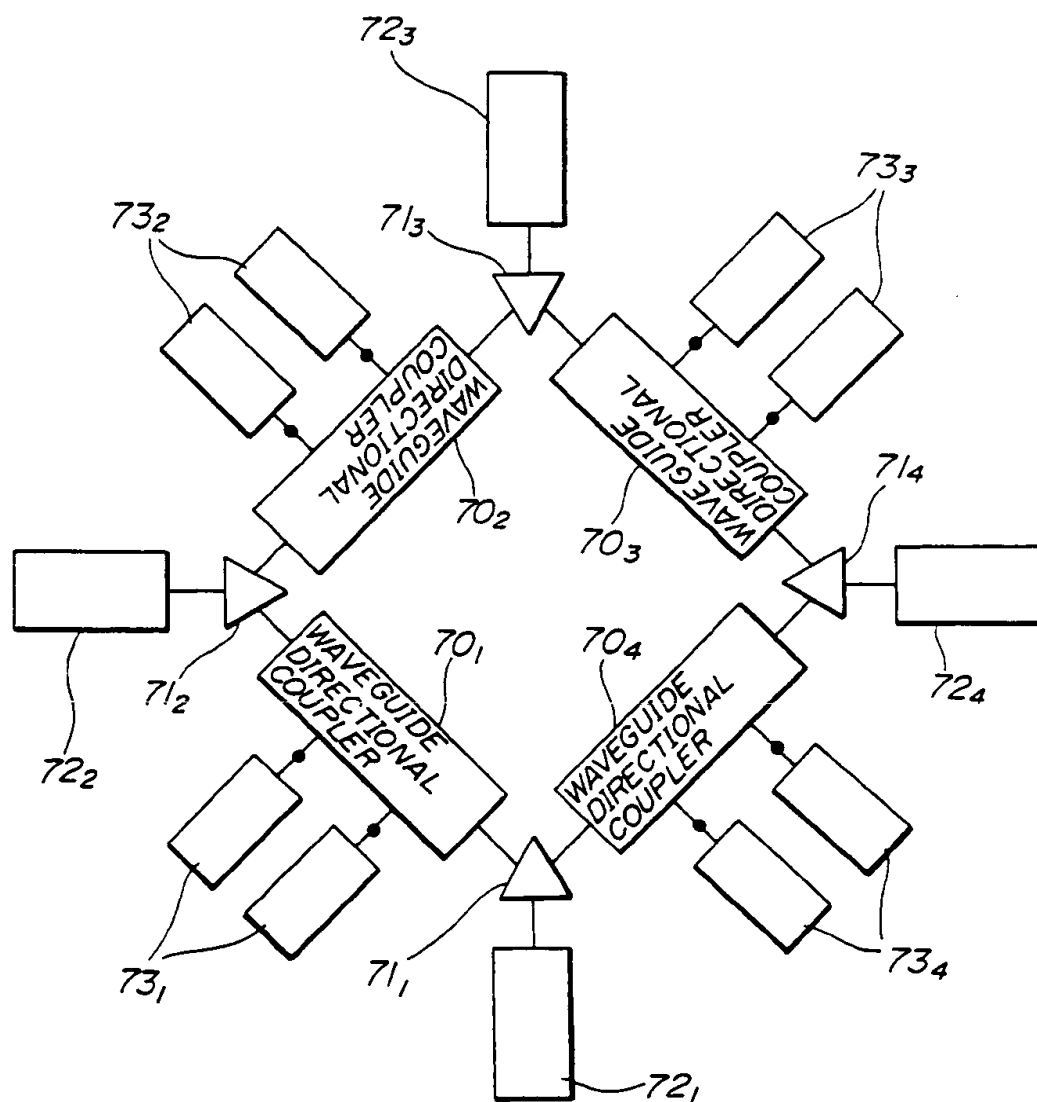


FIG. 13A

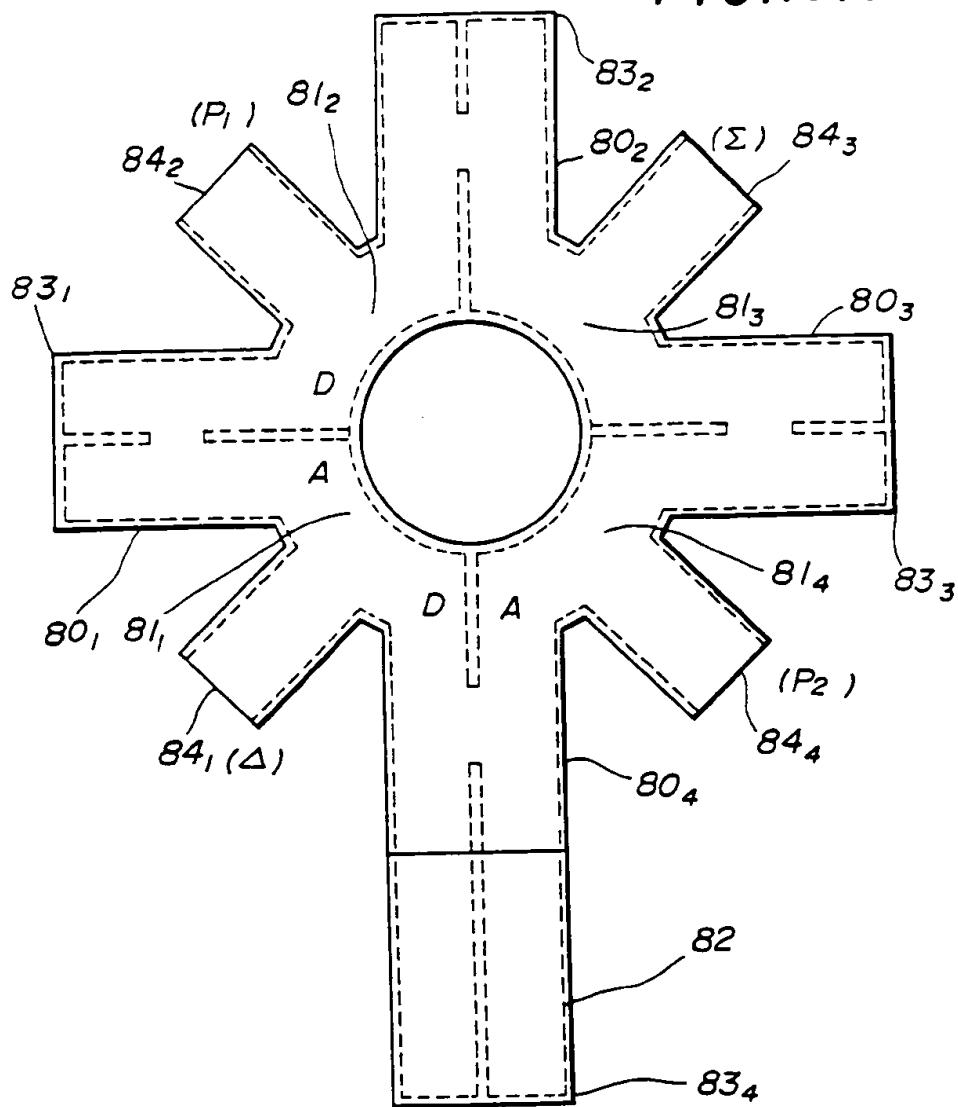
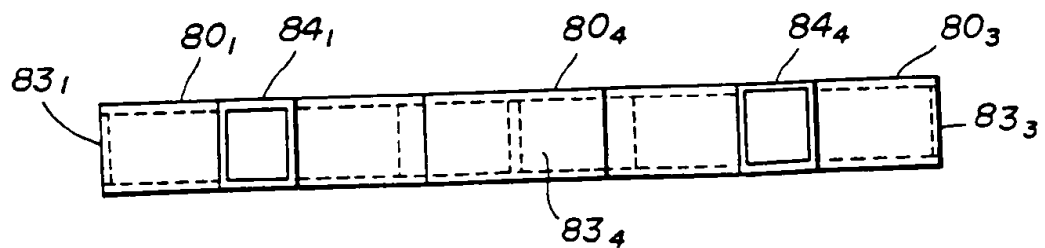
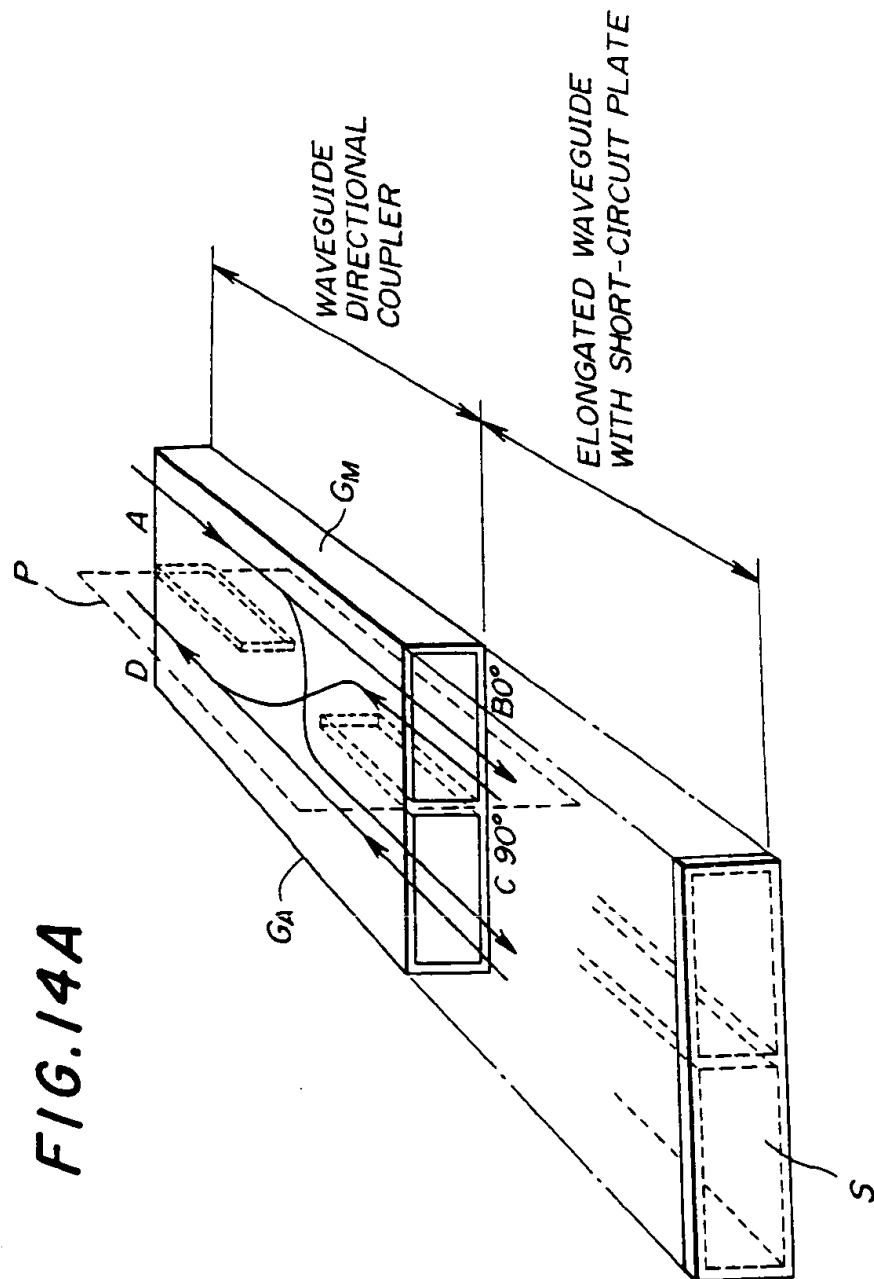


FIG. 13B





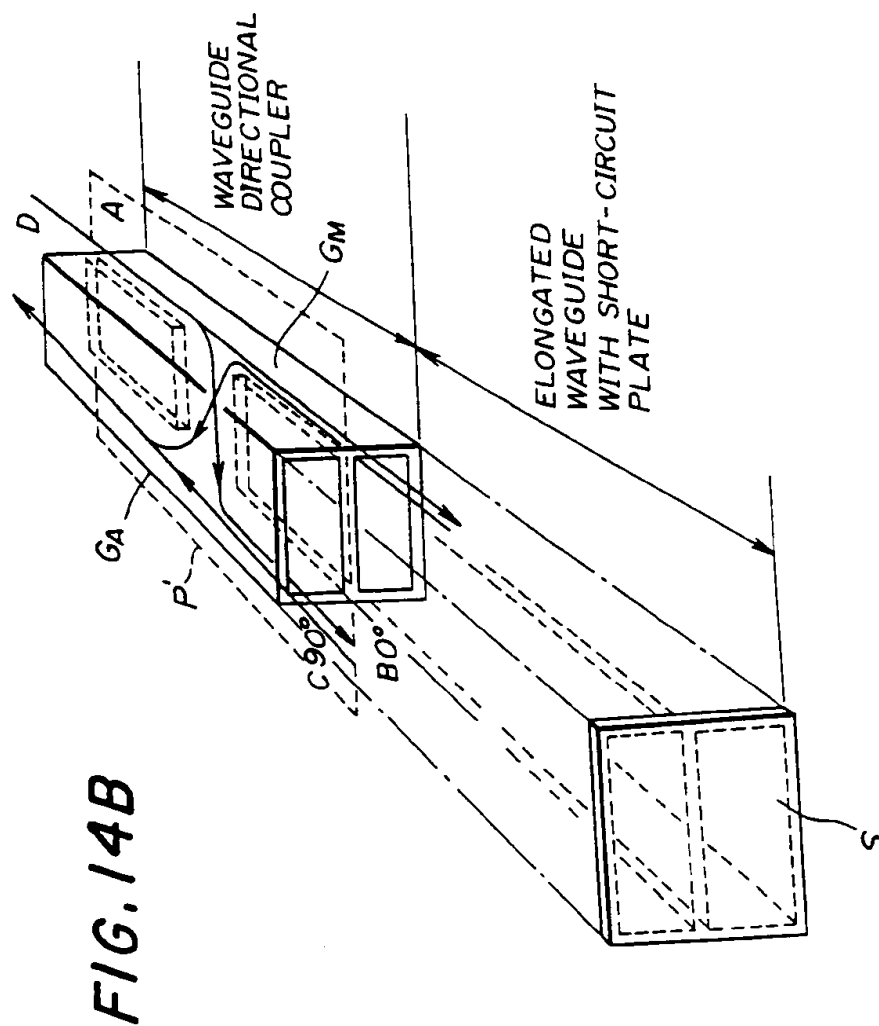


FIG. 14B

FIG. 15A

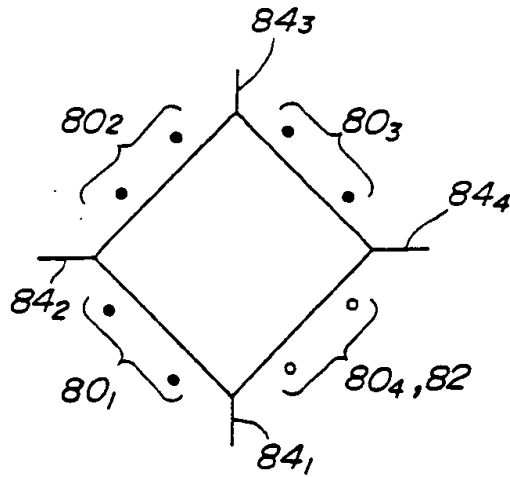


FIG. 15B

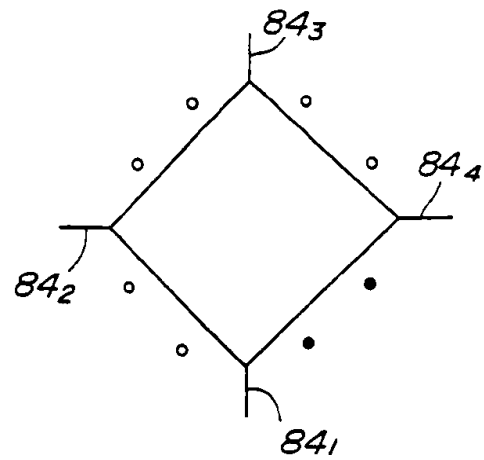


FIG. 16

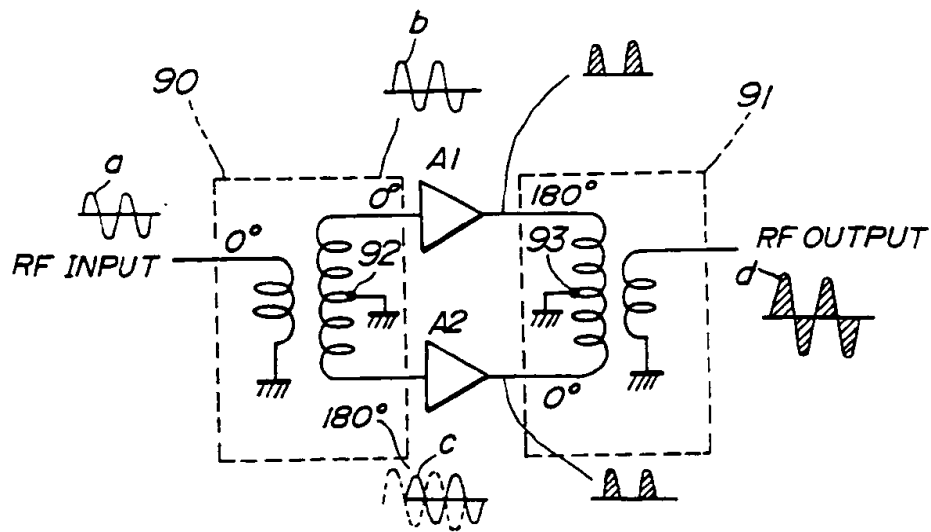


FIG. 17

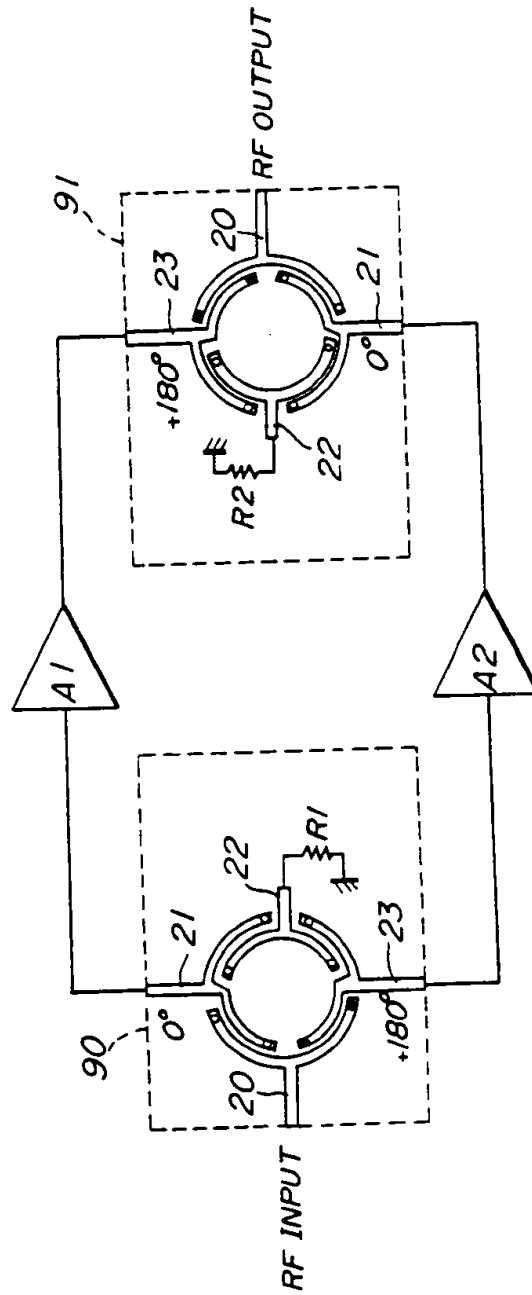


FIG. 18

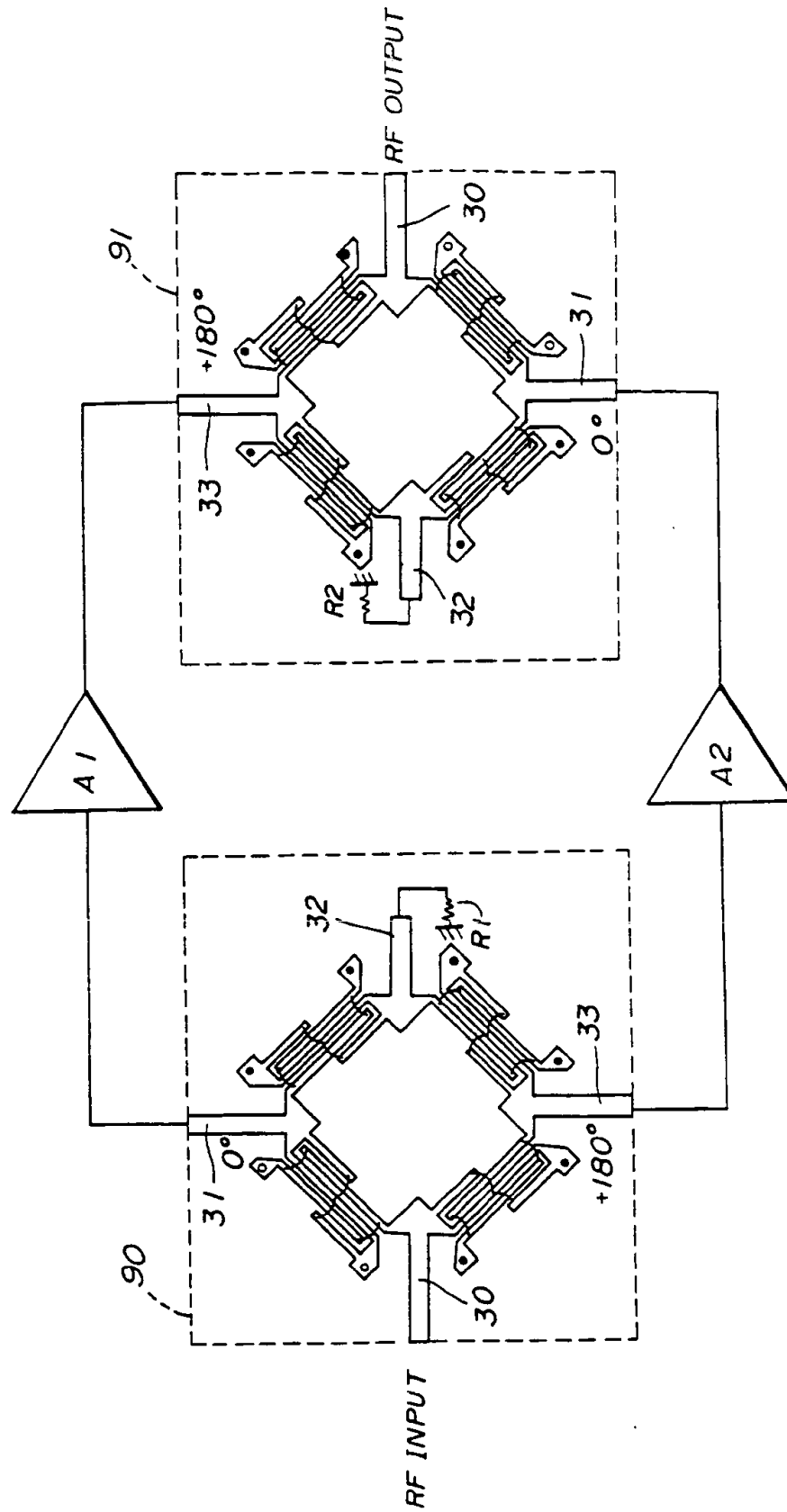
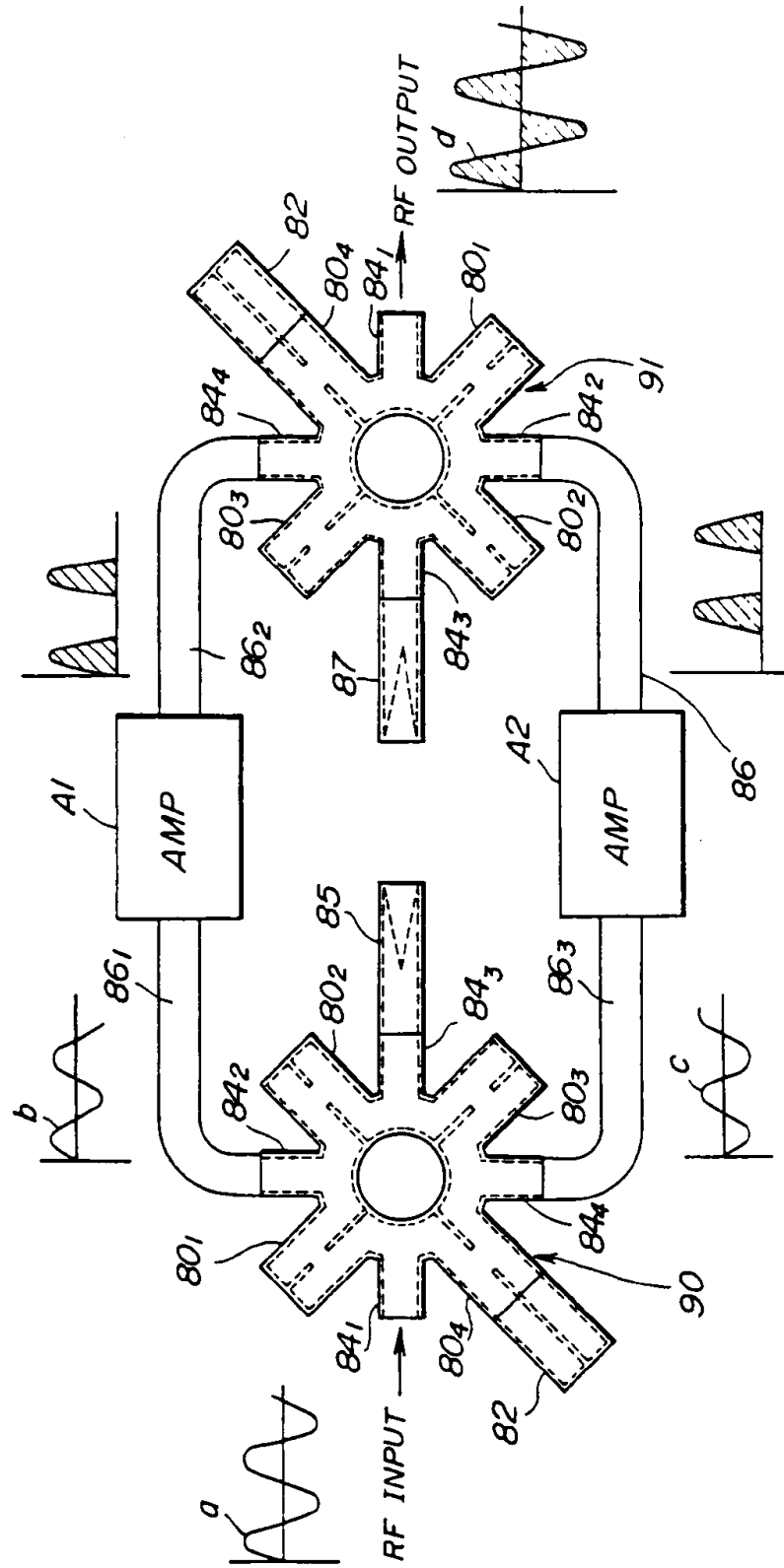


FIG. 19







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H03F 3/26, H03F 3/60

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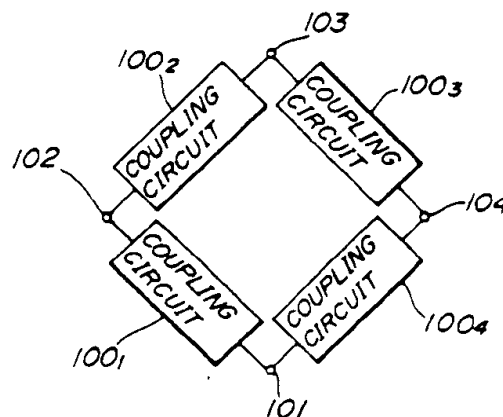
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(54) Phase inverter and push-pull amplifier using the same.

(57) A phase inverter includes first, second, third and fourth terminals (101 - 104), a first coupling circuit (100₁) coupled between the first and second terminals, a second coupling circuit (100₂) coupled between the second and third terminals, a third coupling circuit (100₃) coupled between the third and fourth terminals, and a fourth coupling circuit (100₄) coupled between the first and fourth terminals. The first to fourth terminals and the first to fourth coupling circuits are arranged into a ring. The first coupling circuit is of a type different from a type of the fourth coupling circuit. The second and third coupling circuits are identical in type. Two output signals having a phase difference of 180° are drawn from the second and fourth terminal when an input terminal is applied to the first terminal, and an output signal is drawn from the first terminal when two input signals having a phase difference of 180° are applied to the second and fourth terminals. A push-pull amplifier using two phase inverter each having the above-mentioned configuration is also provided.

FIG.3



EP 0 379 202 A3



European
Patent Office

EUROPEAN SEARCH REPORT

Application Number

EP 90 10 1029

DOCUMENTS CONSIDERED TO BE RELEVANT																	
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)														
A	EP-A-0 180 011 (GTE TELECOMUNICAZIONI S.P.A.) * page 2, lines 9 - 14 ** page 4, lines 5 - 15; figure 4 * - - - -	1.4,6,8,9, 22,24,26	H 01 P 5:16 H 01 P 1:18														
A	TRANSACTIONS OF THE INSTITUTE OF ELECTRONICS AND COMMUNICATION vol. E60, no. 1, January 1977, TOKYO JP pages 44 - 45; I.SAKAGAMI ET AL.: "A constant- resistance power divider using coupled lines and its opti- mum design for the wide bandwidth case" * page 44, right-hand column, lines 1 - 8; figure 1 * - - - -	1.6,8,9, 24,26	H 03 F 3:26 H 03 F 3:60														
A	IEEE TRANSACTIONS ON MICROWAVE THEORY AND TECHNIQUES vol. 16, no. 6, June 1968, NEW YORK US page 361 S.MARCH: "A wideband stripline hybrid ring" * figure 2 * - - - -	1.6,9,24															
A	EDN ELECTRICAL DESIGN NEWS vol. 25, no. 18, October 1980, NEWTON, MASSACHUSETT pages 107 - 110: H.HOWE: "Basic parameters hold the key to microstrip- amplifier design" * page 110, left-hand column, line 1 right-hand column, line 13; figures 6-8 * - - - -	12															
A	WO-A-8 702 189 (HUGHES AIRCRAFT COMP.) * page 5, line 14 - page 8, line 8; figure 2 * - - - -	13	TECHNICAL FIELDS SEARCHED (Int. Cl.5) H 01 P H 03 F														
A	IEEE TRANSACTIONS ON MICROWAVE THEORY AND TECHNIQUES vol. 34, no. 12, December 1986, NEW YORK US pages 1318 - 1325; J.R.LANE ET AL.: "High-efficiency 1-,2- and 4-W class-B FET power amplifiers" * page 1321, left-hand column, lines 6 - 14; figure 15 * - - - -	19,28															
E	EP-A-0 383 311 (FUJITSU LTD.) * page 3, line 30 - page 4, line 37 ** page 6, lines 2 - 16; figures 3-5, 9A-C * - - - - -	1-9,12, 19-26															
The present search report has been drawn up for all claims																	
Place of search The Hague		Date of completion of search 18 February 91	Examiner DEN OTTER A.M.														
<table border="0"><tr><td>CATEGORY OF CITED DOCUMENTS</td><td>E: earlier patent document, but published on, or after the filing date</td></tr><tr><td>X: particularly relevant if taken alone</td><td>D: document cited in the application</td></tr><tr><td>Y: particularly relevant if combined with another document of the same category</td><td>L: document cited for other reasons</td></tr><tr><td>A: technological background</td><td>&: member of the same patent family, corresponding document</td></tr><tr><td>O: non-written disclosure</td><td></td></tr><tr><td>P: intermediate document</td><td></td></tr><tr><td>T: theory or principle underlying the invention</td><td></td></tr></table>				CATEGORY OF CITED DOCUMENTS	E: earlier patent document, but published on, or after the filing date	X: particularly relevant if taken alone	D: document cited in the application	Y: particularly relevant if combined with another document of the same category	L: document cited for other reasons	A: technological background	&: member of the same patent family, corresponding document	O: non-written disclosure		P: intermediate document		T: theory or principle underlying the invention	
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